**General Objective:**

Upon completion of this lab, the student will be able to:

1. Improve the Critical Frequency High of an amplifier circuit using Emitter, Shunt, & Series Peaking.
2. Discuss and show calculations for each circuit modification.
3. Measure and document the results of each circuit modification.
4. Identify and reconcile any discrepancies between a calculated and measured result.
5. Demonstrate the proper use of the oscilloscope, DMM, and sweep audio generator to obtain accurate measurements.

**References:**

* Theory notes
* First Year Text & Lab books
* [Tektronix AFG1022 Function Generator Excerpt](https://drive.google.com/file/d/1g95P8_0zsS-zD27CyxFKEkg3zqlOGmz9/view?usp=sharing)
* [Multistage Schematic](https://drive.google.com/file/d/1fH8i4o53Zl2bZaMHP3KNaGaTBR0mlysM/view?usp=sharing)
* [Multistage PCB Layout](https://drive.google.com/file/d/1fJW5RPRtUVS87FdKih90xuCrNwghQMtY/view?usp=sharing)

**Check-Off Sheet:**

* [Check-Off Sheet](https://drive.google.com/file/d/1gfg09oh1spySt6cdQ1rV8ITwARv0C-LV/view?usp=sharing)

**Specific Objectives:**

Notes.

* 1. See Multistage Schematic
  2. DC supply voltage to J2 is 25V

1. Emitter, Shunt, and Series Peaking:
   * Emitter Peaking
     1. Explain what emitter peaking is and how it works.
     2. Calculate the capacitor needed for emitter peaking.
     3. Draw a bode plots comparing the original response and the effect of emitter peaking. Calculate the improvement factor.
     4. **Instructor Check**
     5. Measure the effects of emitter peaking.
     6. Observe frequency response using the sweep generator. Document measured frequency response (bode plot).
     7. Annotate in a Table Calculated vs. Measured data.
     8. Identify any discrepancies and provide analysis/justification for discrepancies.
     9. **Instructor Check**
   * Shunt Peaking - Repeat above steps for Shunt Peaking.
   * Series Peaking - Repeat above steps for Series Peaking.
2. Employ “Peaking” to achieve a 3.579545Mhz signal that is within the pass band of the amplifier.
3. Additional documentation requirements:
4. List five factors that limit the high frequency response of transistor amplifiers.
5. List four factors that limit the low frequency response of transistor amplifiers.
6. List methods that could be incorporated in an amplifier to improve the high frequency response.
7. List methods that could be incorporated in an amplifier to improve the low frequency response.
8. Complete Conclusion and submit completed Check-Off sheet and Lab writeup in Moodle.